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Attorney Docket No.: SEL 249

8/18/02
31A
B05/23/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hajime KIMURA

Serial No.: 09/829,114

Filed: April 9, 2001

For: Semiconductor Device And Method
Of Driving The Same

Examiner:

Art Unit: 2811

Commissioner for Patents
Washington D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
the Commissioner for Patents, Washington, D.C. 20231 on
April 24, 2002

Rachelle Hamerquist
Rachelle Hamerquist 4-24-02

Signature Date

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PRELIMINARY AMENDMENT A

Prior to examination, please enter the following amendments in the above-identified application as follows:

IN THE CLAIMS:

Please add the following new claims:

67. (New) A driving method of a semiconductor device comprising an electric discharging

transistor, an n-channel amplifying transistor and an n-channel biasing transistor, wherein a drain terminal of the n-channel amplifying transistor is connected to an amplifying side power source line,

a source terminal of the n-channel amplifying transistor is connected to a drain terminal of the n-

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01 FC:103
02 FC:102

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